

PRINTER RUSH
(PTO ASSISTANCE)

Application : 09/870943 Examiner : Garber GAU : 2612
From : PAP Location : (IDC) FMF FDC Date : 4/15/05
Tracking # : 06071789 Week Date : 11/3/05

DOC CODE	DOC DATE	MISCELLANEOUS
<input type="checkbox"/> 1449	_____	<input type="checkbox"/> Continuing Data
<input type="checkbox"/> IDS	_____	<input type="checkbox"/> Foreign Priority
<input type="checkbox"/> CLM	_____	<input type="checkbox"/> Document Legibility
<input type="checkbox"/> IIFW	_____	<input type="checkbox"/> Fees
<input type="checkbox"/> SRFW	_____	<input type="checkbox"/> Other
<input checked="" type="checkbox"/> DRW	<u>06/01/2001</u>	
<input type="checkbox"/> OATH	_____	
<input type="checkbox"/> 312	_____	
<input type="checkbox"/> SPEC	_____	

Attention: Chief Drafts person

[RUSH] MESSAGE: Please supply a new drawing sheet for
Figure 3 - lead lines missing reference numbers.

Thank you

[XRUSH] RESPONSE: _____

OK by Draftman

INITIALS: J

NOTE: This form will be included as part of the official USPTO record, with the Response document coded as XRUSH.

REV 10/04

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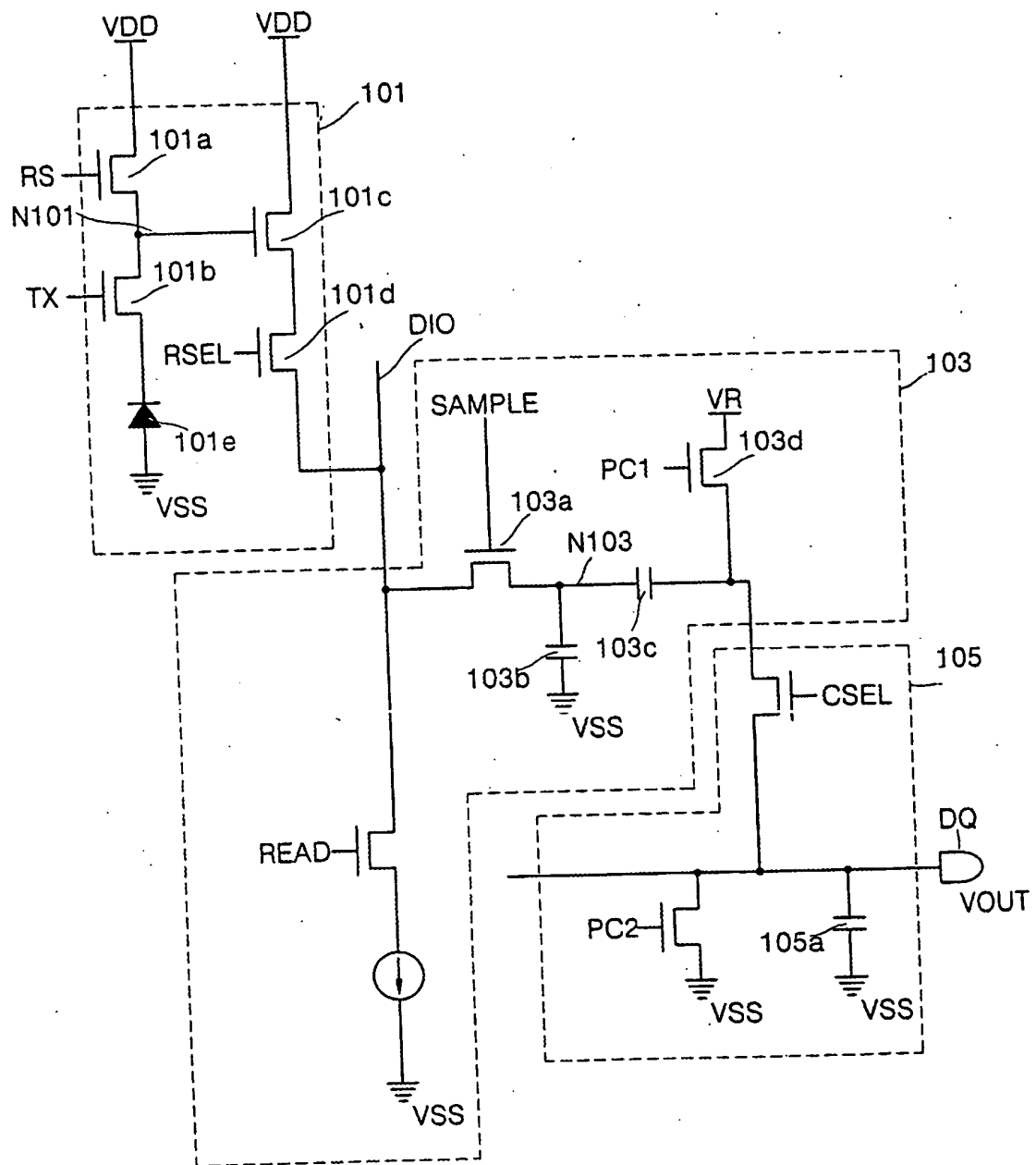
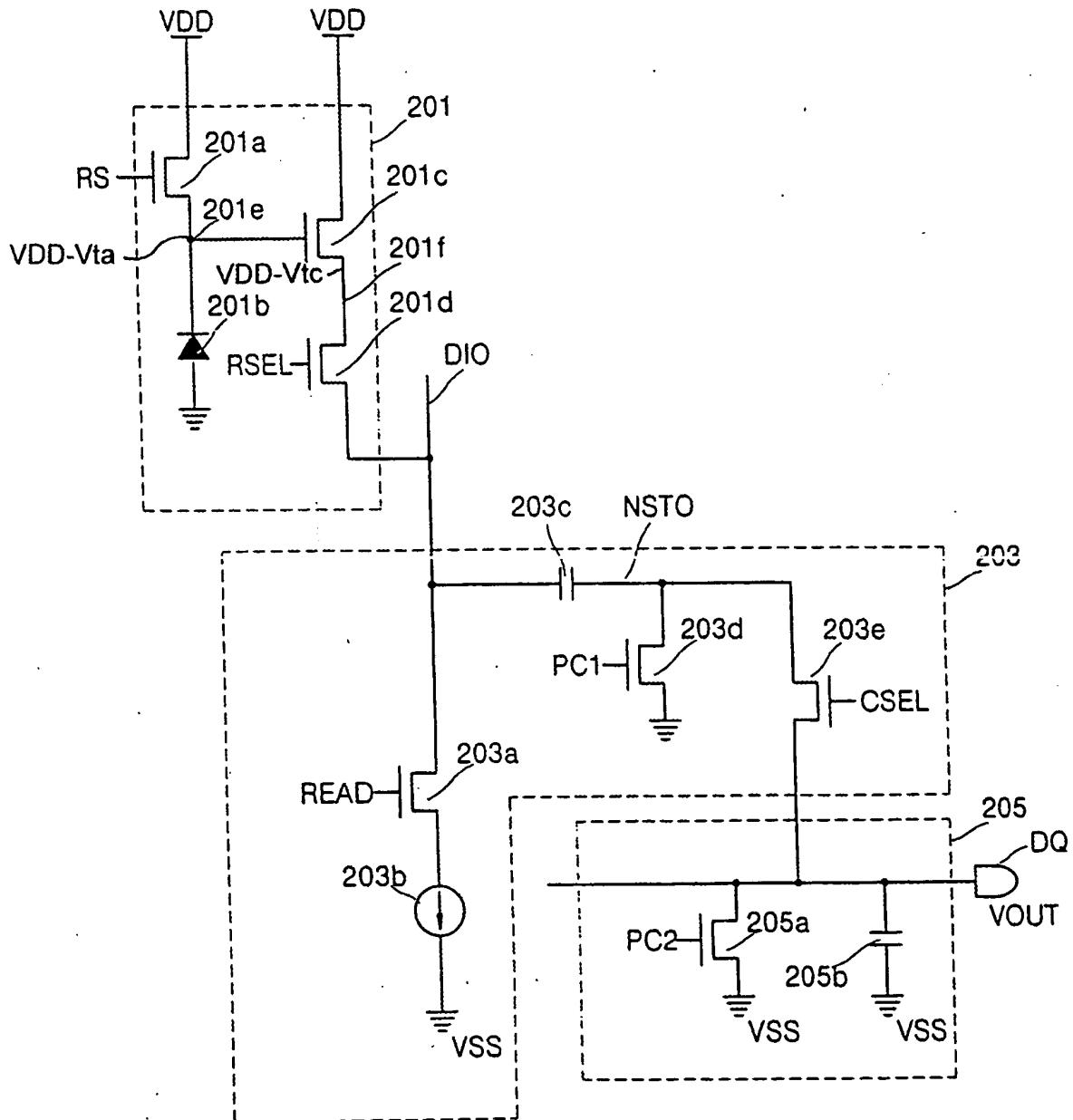
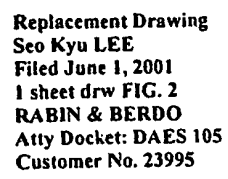


FIG.1
(PRIOR ART)



The timing diagram illustrates the sequence of signals for the 28C01B device. The signals are:

- RS**: Reset signal, active low. It transitions from high to low at time t_4 and returns to high at time t_6 .
- RSEL**: Read/Select signal, active low. It transitions from high to low at time t_2 and returns to high at time t_5 .
- READ**: Read signal, active low. It transitions from high to low at time t_5 and returns to high at time t_7 .
- PC1**: Program Counter 1 signal, active low. It transitions from high to low at time t_1 and returns to high at time t_3 .
- CSEL**: Command Select signal, active low. It transitions from high to low at time t_7 and returns to high at time t_7 .
- PC2**: Program Counter 2 signal, active low. It transitions from high to low at time t_6 and returns to high at time t_6 .
- DIO**: Data Input/Output signal. It is high during the initial phase and transitions to low at time t_1 . The voltage level is labeled V_{rst} and V_1 .
- NSTO**: Not Strobe signal, active low. It is high during the initial phase and transitions to low at time t_1 . The voltage level is labeled V_{sig} .

The diagram also shows the voltage levels V_{rst} , V_1 , and V_{sig} for the DIO and NSTO signals. The signals are shown with respect to the VSS (ground) reference.

FIG.3